

### **Examiner's Statement of Reasons for Allowance**

1. This action is responsive to Applicant's supplemental amendment filed on April 14, 2010.

### **Information Disclosure Statement**

2. The Office acknowledges receipt of the Information Disclosure Statement filed February 22, 2010. It has been placed in the application file and the information referred to therein has been considered.

### **Examiner's Statement of Reason(s) for Allowance**

3. Claims 1-6, 17-18, 21-33 are allowed.
4. The following is an examiner's statement of reasons for allowance:  
The prior arts of record: **Baluja** et al., teaches method of automated code generation. The method includes identifying an event related to the execution of an application, generating code related to the event using an automated process, and integrating the code into the application. It is emphasized that this abstract is provided to comply with the rules requiring an abstract which will allow a searcher or other reader to quickly ascertain the subject matter of the technical disclosure. **Fam**, teaches method includes providing an executable code that is executable on a series of mobile phone models, and loading the executable code into a memory of a selected mobile phone. The method further includes providing a plurality of parameter sets having a uniform data format, each parameter set corresponding to at least one mobile phone, and then, determining the parameter set corresponding to the selected mobile phone. **Hayashi**, teaches a language translating and linking

system for translating and linking into a load program a plurality of source programs which include a calling program, having a first number of calling parameters, and a called program, having a second number of called parameters, the calling parameters must correspond to the respective called parameters. For this purpose, a compiler generates first and second parameter information representative of the calling and the called parameters from the calling and the called programs, respectively. A compiling unit memorizes the first and the second parameter information as first and second memorized parameter information.

**Lo**, discloses method and system for generating programming code and/or configuration data for programmable controller and the networks on which they operate is disclosed. In one embodiment, programming code is generated on a centralized server having a web-enabled engineering tool. **Chong et al.**, teaches workflow code generator for generating executable code for multi-channel and/or multi-modal applications. The code generator may include a parser for reading application input files and creating internal representations of declarative statements within the input files. The code generator may further include a model analyzer, which processes the internal model to detect errors, perform optimization, and prepare for outputting the result. The code generator uses a symbol or mapping table for storing references to resources that have been used by the input application. **Shoumura et al.**, teaches a program development support system so adapted as to integrally manage a variety of information necessary for the development of a program by creating necessary links between information such as source programs to be employed for the development of the program, program parts, tools, specifications, handling persons and so on, by managing such information as resource information and by retaining attribute information of the

links. **Freeman**, teaches an apparatus that generates a sequence of code instructions for execution by a programmable processor to solve a problem. In includes generating a sequence of variable value data corresponding to postulate solutions to such problem; testing the postulate solution data in a relationship to determine whether or not they correspond to the solution to the problem; and, in the event that the test cannot be logically evaluated, storing data defining a decision forming part of the sequence of instruction codes, and generating a plurality of branches of the sequence to be performed depending upon the results of the decision including more than one possible branch from the decision to be taken in the event of the same outcome of the decision, and for selecting one of the branches. **Coppinger** et al., teaches a communication system includes a development sector for registering a plurality of wireless devices, a business sector for controlling utilization of an application program and for receiving application data, a service sector for deploying the application program and for providing the application data to the business sector in accordance with communication with at least one wireless device of the plurality; and a mobile sector comprising the plurality of wireless devices, each device for receiving the application program deployed by the service sector, executing the application program in response to the business sector, and communicating with the service sector to support provision of the application data to the business sector. The wireless device, such as a cell phone, personal digital assistant, or palm top computer may include an auxiliary device such as a bar code scanner, a magnetic stripe card reader, or a printer. **Narisawa** et al., discloses a code generation system is provided which optimizes a code generation for a control system applicable to an embedded control system without the need to increase its memory capacity. A total control

unit 110 causes to read a model diagram and an operation diagram which depict a software specification stored in a memory 104, and starts specification analysis unit 106 to execute lexical and grammatical analyses thereof. **Portwood** et al., teaches method for developing an application for interacting with one or more external objects where a Workbench program receives the initial instructions from the software developer and queries the external objects which are to interact with the application to obtain sufficient information to interact with the external objects. A Built Project is produced by the Workbench which contains a series of instructions for a Runtime program to produce an application which interacts with the external objects. The Runtime takes the Built Project and using a runtime environment such as the JRE, builds the desired application. The purpose of the Runtime is to reproduce the user interface that was designed using the Workbench and connect that user interface to the specified external objects. **Frey** et al., teaches a software development tool for embedded computer systems, and is based on a repository of configurable, pre-programmed software components, together with associated tools for user selection and configuration of the components and a code generator for extracting relevant source code based on the configuration settings. Each software component, called embedded system infrastructure component (ESIC), is a self-contained object comprising a modular code base and associated configuration structure related to an infrastructure function in a hardware-independent, non-operating-system software infrastructure for an embedded computer system. **Van Gennip** et al., discloses a compiler and linker for analyzing the structures of complex data stored in memory when a print statement specified in source code refers to those complex data structures, and then generating executable code which will print the complex data when a data processing system

executes the executable code. **Takashima et al.**, teaches accepting unit accepts various parameters required to create a program from a parameter managing unit. A producing unit loads a model of a program code from a model storing unit on the basis of the parameters and replaces a part of the program code with the parameters to produce a source code. An instructing unit detects that the producing unit produces the source code to start a compiling unit. The compiling unit compiles the source code produced by the producing unit to create a research program. In this manner, a research program can be automatically created without advanced special knowledge. New art made of record: US 2003/0177501 A1 by **Takahashi et al.**, teaches a display information program for laying out the screen of a television set includes a part which defines a plurality of display areas and lays out the display areas and a part which defines an external information source at which information to be displayed in the defined display areas is originated and allows other information to be attached thereto. The displaying of information from a particular external information source is defined. Using the rule of URI, the external information is laid out on a display screen with layout freedom. However, none of them, taken alone or in combination, teaches the features in such a manner as recited in each of the independent claims 1, 17, 18, 32, and 33.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chih-Ching Chow whose telephone number is 571-272-3693. The examiner can normally be reached on 8:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on 571-272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Chih-Ching Chow/  
Examiner, Art Unit 2191  
4/16/10

**/Wei Y Zhen/**

**Supervisory Patent Examiner, Art Unit 2191**